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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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BERND KOWALSKI

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EXAMINER

BROWN, CHRISTOPHER J

ART UNIT

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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 09/403,689	Applicant(s) KOWALSKI ET AL.	
	Examiner CHRISTOPHER J. BROWN	Art Unit 2134	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 26 March 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 8-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 8-18 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 3/26/2008 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Arguments

Applicant's arguments with respect to claims 8-18 have been considered but are not persuasive.

As per the USC 112 rejection of Claim 12. The applicant has not amended claim 12 in a satisfactory manner. As stated in the prior office action, the instant specification bottom of page 6, top of page 7, and page 8 lines 10-14, state the intention of the invention is opposite that which is stated by claim 12. The specification specifically states the “encryptor” is software, and does not rely on a chip card or other external crypto-hardware.

As per USC 103 rejection of claims 8, and 18 Applicant argues that the combination of Thompson US 5,805,204 and Powar US 6,285,991 does not teach a Vernam key which is regenerated, or installing a storage space and one of a cryptographic cipher in a crypto-module, where the crypto module is separate from an encryptor, and performing the encryption operations in the encryptor. Applicant also submits the references are not combinable.

Examiner argues that the combination as stated teaches every limitation of claim 8 and 18. Specifically the Examiner cites Thompson column 7 lines 37-50, as stated in the

rejection below. Thompson teaches regenerating the Vernam key (seed key) by encrypting the random number with an imbedded key. Thompson teaches regenerating the key with a symmetric cipher on a crypto module (smart card), which must have been installed in a storage space on said crypto module. Thompson teaches passing the Vernam key (seed key) to the encryptor (decoder microprocessor) where the encryption operations are performed.

The Examiner argues that one of ordinary skill in the art would look to Powar for the asymmetrical cipher, because the references are of analogous arts, and the public key infrastructure system provides an additional layer of security for data transmission that is well known in the art.

The rejection below is substantially similar to the previous non-final rejection.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claim 12 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of

the claimed invention. Amended Claim 12 states that the encryptor includes at least one of a chip card, a multifunctional PC interface and a PCMCIA module. This does not appear to agree with the instant specification.

The instant specification states that “the external crypto hardware including for example either of a chipcard or of a multifunctional PC interface adapter or PCMCIA module with built in special crypto-hardware or a built in special chipcard. The encryptor on the other hand is implemented as a conventional PC with software or another terminal.....” at the bottom of page 6, and top of page 7. Page 8 lines 10-14 also state the encryptor is composed of PC software, or a terminal”. These configurations are illustrated in Fig 6, and 7.

It appears to the examiner from the instant specification that the purpose of the invention is to have a general “encryptor” to implement the simple Vernam cipher, and a separate “crypto module” in a chipcard or PCMCIA module for complex cryptographic functions. The applicant is encouraged to amend the subject matter to comply with the written description. Claim 15 appears to state the invention in accordance with the instant specification clearly. The examiner is interpreting claim 12’s encryptor “includes” as encompassing elements the encryptor is connected too.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 8-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Thompson US 5,805,204 in view of Powar US 6,285,991

As per claims 8 and 18, Thompson teaches generating a Vernam key (seed key) via a symmetrical cipher (DES), the generating being aided by using a secret key (imbedded key) and a variable parameter (random number) or seed) (encrypting the generated random number using DES and the imbedded key to create a seed key) (Col 7 lines 18-28) It is well known that a Vernam key contains the properties of having a length that is equal to a length of a message to be protected, the secret key having a defined key length (64 bit DES), the variable parameter having a length which is a function of the defined key length (length of message). Thompson teaches encrypting, the message(teaches encrypting actual transmitted data using the seed key) (Col 7 lines 25-30). Thompson does not specify the Vernam cipher but only an “algorithm”. The examiner asserts that the Vernam cipher is well known in the art (shown in the Handbook of Applied Cryptography page 21 by Menezes)

Thompson teaches communicating, from a sending point to a receiving point, a secret key ID (imbedded key ID) and the variable parameter (random number) (transmit the key ID and random number).

Thompson teaches regenerating the Vernam key (encrypting the random number using the imbedded key) (Col 7 lines 37-45).

Thompson teaches a storage space and one of a symmetrical cipher in a crypto-module, the crypto-module being separate from an encryptor (a smart card implements the DES algorithm to create the seed key) (Col 7 lines 40-45). Thompson teaches performing encryption operations via the Vernam cipher in the encryptor (teaches performing decryption of the data using the seed key then passing the sseed key to the microprocessor which performs decryption) (Col 7 lines 40-51).

Thompson fails to teach sending the key and random number via at least one of (A) a secure channel separate from a message- transmission path and (B) the message-transmission path, the message-transmission path being secured via an asymmetrical cipher; regenerating the Vernam key; and decrypting the message.

Powar teaches sending data including a key via a message transmission path secured via an asymmetrical cipher (encrypting data and a session key with the public key of the customer, the customer using the private key to recover the session key, and the session key to decrypt data) (Col 4 line 62 to Col 5 line 13).

It would have been obvious to one of ordinary skill in the art to use the asymmetric encryption of Powar with the system because it provides privacy and security.

As per claims 9-11 The examiner asserts the Vernam cipher is well known in the art as a very simple EXOR operation and is used for its simplicity and ease of use as shown in the Handbook of Applied Cryptography page 21 by Menezes. (Previously Presented).

As per claim 12 Thompson teaches storing the Vernam key (seed key) in the storage space (not explicitly stated, the smart card stores “imbedded keys”, so the smart card contains memory, and the card creates the vernam key, so it is stored upon creation).

Thompson teaches the external crypto module being separate from the encryptor (Fig 7, smart card 11) Thompson teaches the encryptor includes at least one of a chip card, a multifunctional PC interface adapter and a PCMCIA module (smart card) (Col 7 lines 34-36).

Thompson teaches performing Vernam cipher operations exclusively in the encryptor, wherein the encrvptor includes including at least one of a chip card, a multifunctional PC interface adapter and a PCMCIA module (smart card is connected to encryptor where the vernam/seed key is passed and microprocessor decrypts data).

As per claim 13 Thompson teaches the crypto-module is an external crypto- module (smart card) (Col 7 lines 35-40). Thompson teaches the external crypto module being separate from the encryptor (Fig 7, smart card 11) Thompson teaches controlling, via the Vernam cipher, encryption operations in the encryptor (smart card creates the seed key used for encryption operations in the encryptor)(Col 7 lines 37-45).

As per claim 14 Thompson teaches the Vernam key is stored in the encryptor (the seed

key is passed to the encryptor)(Col 7 lines 42-47).

As per claim 15, Thompson teaches generating a Vernam key (seed key) via a symmetrical cipher (DES), the generating being aided by using a secret key (imbedded key) and a variable parameter (random number) or seed) (encrypting the generated random number using DES and the imbedded key to create a seed key) (Col 7 lines 18-28) It is well known that a Vernam key contains the properties of having a length that is equal to a length of a message to be protected, the secret key having a defined key length (64 bit DES), the variable parameter having a length which is a function of the defined key length (length of message). Thompson teaches encrypting, the message(teaches encrypting actual transmitted data using the seed key) (Col 7 lines 25-30). Thompson does not specify the Vernam cipher but only an “algorithm”. The examiner asserts that the Vernam cipher is well known in the art (shown in the Handbook of Applied Cryptography page 21 by Menezes)

Thompson teaches communicating, from a sending point to a receiving point, a secret key ID (imbedded key ID) and the variable parameter (random number) (transmit the key ID and random number).

Thompson teaches regenerating the Vernam key (encrypting the random number using the Imbedded key) (Col 7 lines 37-45).

Thompson teaches a storage space and one of a symmetrical cipher in a crypto-module, the crypto-module being separate from an encryptor (a smart card implements the DES algorithm to create the seed key) (Col 7 lines 40-45). Thompson teaches performing

encryption operations via the Vernam cipher in the encryptor (teaches performing decryption of the data using the seed key then passing the seed key to the microprocessor which performs decryption) (Col 7 lines 40-51).

Thompson teaches the encryptor being capable of coupling to the crypto-hardware (decoder couples to smartcard, Fig 7) Thompson teaches the encryptor including at least one of a personal computer, software and a terminal which implements a Vernam cipher for broad-band applications in software (subscriber unit uses terminal to decrypt data, (Col 7 lines 32-52).

Thompson fails to teach sending the key and random number via at least one of (A) a secure channel separate from a message- transmission path and (B) the message-transmission path, the message-transmission path being secured via an asymmetrical cipher; regenerating the Vernam key; and decrypting the message.

Powar teaches sending data including a key via a message transmission path secured via an asymmetrical cipher (encrypting data and a session key with the public key of the customer, the customer using the private key to recover the session key, and the session key to decrypt data) (Col 4 line 62 to Col 5 line 13).

It would have been obvious to one of ordinary skill in the art to use the asymmetric encryption of Powar with the system because it provides privacy and security.

As per claims 16, and 17, Thompson teaches crypto-hardware (smartcard) and terminal

having an intermediate storage storing the Vernam key (smart card creates the key, thus must store it, and passes a copy to terminal for utilization) (Col 7 lines 35-50).

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to CHRISTOPHER J. BROWN whose telephone number is (571)272-3833. The examiner can normally be reached on 8:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kambiz Zand can be reached on (571)272-3811. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2134

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Christopher J Brown/
Primary Examiner, Art Unit 2134

6/23/08